Amendments to the Specification

Please amend the consecutive two paragraphs beginning on page2, line 19, as follows:

PHY **106** receives a MAC frame from MAC **104**, strips off the 8 octets of preamble and delimiter from the MAC frame, adds a HomePNA PHY header to form a HomePNA PHY frame, and transmits a PHY frame on physical medium ~~108~~ __109__. Fig. 2 illustrates HomePNA PHY framing. A PHY frame comprises Ethernet Packet **202**, and appended to Ethernet Packet **202** is a HomePNA PHY header, comprising SYNC interval **204**, Access ID (Identification) **206**, Silence interval **208**, and PCOM field **210**.

A PHY frame is transmitted on physical medium ~~108~~ __109__ utilizing pulse position modulation (PPM). All PHY symbols transmitted on physical medium ~~108~~ __109__ comprise a pulse formed of an integer number of cycles of a square wave that has been filtered with a bandpass filter. The position of the pulse conveys the transmitted symbol. Differential signaling is employed, in which a pulse and its negative are transmitted on two wires for each transmitted symbol. However, for simplicity of discussion, we consider only one component of the differential signal when describing the signal waveform.

Please amend the paragraph beginning on line 29, page 5, as follows:

Fig. 7 provides a high-level circuit model of an embodiment of the present invention. Capacitor **702** is an integrating capacitor that is charged by current sink **704**. Current sink **704** is a voltage controlled current sink responsive to input voltage V_{in} at input terminal (or port) **706**. Input voltage V_{in} may be a differential input voltage, although only one terminal to current sink **704** is explicitly shown. Input voltage V_{in} is the voltage propagated on physical medium ~~108~~ __109__ (the home phone lines) and received by a PHY.